C. V. RAMAN POLYTECHNIC, BHUBANESWAR

LESSON PLAN

Session (2026-2027)

Discipline: Electronics & Telecommunication Engineering	(Common to 1st Year) Winter-2026	**Name of the Faculty:** Sabyasachi Patra Asst. Prof **Email ID:** sabyasachi.patra@cvrp.edu.in
Subject: FEEE **Course Code:** TH 4(a)	**No. of Days/week:** 02	**Start Date: 17/08/2026** **End Date: 17/04/2027**

Week	Class Day	Theory Topics
1st	1st	Introduction to Electronics, Classification of Electronic Components
	2nd	Passive Components: Resistance – Concept, Units, Types, Simple Problems
2nd	1st	Capacitors – Definition, Types, Working, Simple Problems
	2nd	Inductors – Definition, Types, Working, Simple Problems
3rd	1st	Active Components – Overview and Applications
	2nd	Diodes – PN Junction Diode: Construction & Working
4th	1st	Special Diodes – LED, Zener Diode, Characteristics & Applications
	2nd	Transistors (BJT) – Definition, Types, Working Principle
5th	1st	FET – Construction, Working, Applications
	2nd	MOS, CMOS – Concept, Difference & Applications
6th	1st	Signals – DC & AC signals, Voltage & Current
	2nd	Periodic & Non-Periodic Signals, Signal Waveforms
7th	1st	Average, RMS, Peak Values of Signals
	2nd	Ideal & Non-Ideal Sources, Independent & Dependent Sources
8th	1st	Introduction to Operational Amplifiers
	2nd	Ideal Op-Amp – Characteristics
	1st	Practical Op-Amp – Limitations

Scanned with OKEN Scanner

9th	2nd	Open Loop & Closed Loop Configurations
10th	1st	Op-Amp as Amplifier
	2nd	Op-Amp as Adder
11th	1st	Op-Amp as Integrator
	2nd	Op-Amp as Differentiator
12th	1st	Introduction to Boolean Algebra
	2nd	Electronic Implementation of Boolean Operations
13th	1st	Logic Gates – Functional Block Approach
	2nd	Number Systems – Simple Problems
14th	1st	Storage Elements – Flip-Flops (Functional Approach)
	2nd	Counters – Ripple Counter
15th	1st	Up/Down & Decade Counters
	2nd	Introduction to TTL Digital IC Gates, Course Revision

Signature of the Faculty

Signature of the H.O.D

Scanned with OKEN Scanner