

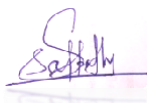
C.V.RAMAN POLYTECHNIC, BHUBANESWAR

LESSON PLAN Session (2025-2026)


Discipline: Electronics & Telecommunication Engineering	Semester: 4 th Semester, Summer/2026	Name of the Faculty: Suchismita Satpathy, Asst. Prof. Email ID: suchismita@cvrp.edu.in
Subject: MICROPROCESSORS & MICROCONTROLLER (ETCPC206)	No. Of Days/Week: 03	Start Date: 22/12/2025 End Date: 18/04/2026

Week	Class Day	Theory Topics
1st	1st	Architecture of 8085 Microprocessor
	2nd	Functional Block Diagram – Registers
	3rd	ALU, Bus systems
2nd	1st	Timing and control signals
	2nd	Machine cycles
	3rd	Timing diagrams.
3rd	1st	Programming of 8085 Instruction formats
	2nd	Programming of 8085 Instruction formats
	3rd	Addressing modes
4th	1st	Instruction set
	2nd	Need for Assembly language
	3rd	Development of Assembly language programmes.
5th	1st	Development of Assembly language programmes.
	2nd	Memory Interfacing Interface requirements
	3rd	Address space partitioning
6th	1st	Buffering of Buses – timing constraints
	2nd	Memory control signals
	3rd	Read and write cycles
7th	1st	Interfacing SRAM, EPROM and DRAM sections.
	2nd	I/O Interfacing Memory mapped I/O Scheme
	3rd	I/O mapped I/O scheme – Input and Output cycles
8th	1st	Simple I/O ports – Programmable peripheral interface (8255)
	2nd	Data transfer schemes: Programmable data transfer
	3rd	DMA data transfer – Synchronous, Asynchronous and interrupt driven data transfer schemes

9th	1st	Interfacing – Simple keyboards and LED displays. DMA controller 8237
	2nd	Applications- Interfacing of A/D converters (ADC 0800/ADC 0808/ADC 0809)
	3rd	Interfacing of D/A converters (DAC 0800)
10th	1st	Waveform generators
	2nd	Multiplexed seven segment LED display systems – Measurement of frequency,
	3rd	Phase angle and power factor
11th	1st	Traffic light controller – Stepper motor Control
	2nd	Intel 8051 Microcontroller Architecture of 8051
	3rd	Memory Organization – Addressing modes
12th	1st	Instruction set
	2nd	Boolean processing
	3rd	Simple programmes.
13th	1st	8051 Peripheral Functions 8051 interrupt structures
	2nd	Timer and serial functions – parallel port features
	3rd	Modes of operation – Power control
14th	1st	features – Interfacing of 8051
	2nd	Typical applications – MCS 51 family features 8031/8051/8751
	3rd	Doubt clearing & Problem Solving class.
15th	1st	Doubt clearing & Problem Solving class.
	2nd	Previous Year Question Discussion.
	3rd	Previous Year Question Discussion.



Concerned Faculty



H.O.D.