C.V. RAMAN POLYTECHNIC BBSR



| Discipline: Diploma | Semester: 5th | Name of the Teaching Faculty: Jagadish |
|------------------------------|---------------------------------------|---|
| Branch: CSE | | Mahanta |
| Subject: Computer Hardware & | No. of Days/per | Semester From Date: 14/07/2025 To Date: |
| Maintenance | week class | 15-11-25 |
| (Th-4) | allotted: 60days/ 4 class per week | No. of Weeks: 15 |
| Week | Class Day | Theory |
| 1 st | 1 st | Introduction about computer Centre and management |
| | 2 nd | Discussion about manpower engaged in computer center |
| | $3^{\rm rd}$ | Need of Management in Computer Centre |
| | 4 th | Types of Jobs carried out in computers in an organization |
| 2 nd | 1 st | Duties and responsibilities of personnel involved |
| | 2^{nd} | Need of Training of Staff |
| | $3^{\rm rd}$ | Idea about Various makes of Computers |
| | 4 th | Introduction about site preparation and how to select a suitable site |
| 3 rd | 1 st | Layouts of computer Centre |
| | 2 nd | Need of False Roofing in computer center |
| | $3^{\rm rd}$ | Need of Air Conditioning in computer Centre |
| | 4 th | Need of Dust Proofing in computer Centre |
| 4 th | 1 st | Brief discussion about Power Conditioning equipment |
| | 2^{nd} | CVT and its principle of functioning |
| | 3 rd | UPS and its principle of functioning |
| | 4 th | Isolation Circuits and its principle of functioning |
| 5 th | 1 st | Components and slots - Processor socket, memory sockets |
| | 2 nd | Components and slots - Chip sets, Key Board/Mouse Connectors |
| | $3^{\rm rd}$ | Components and slots - Jumpers, Pin Connectors |
| | 4 th | Mother architecture |
| 6 th | 1 st | Mother Block Diagram |
| | 2 nd | Processors - Core2 Duo Processor, Quad Core Processor |
| | 3 rd | Processors - Core i3,i5,i7 |
| | 4 th | Processors - AMD A10 series, Xeon Processor |
| 7 th | 1 st | Chip Sets in motherboard |
| | 2 nd | Bus Standards: PCI (Peripheral Components Interconnect) |
| | 3 rd | Bus Standards: AGP (Accelerated Graphic Port), |
| | 4 th | Color Codes for Devices/ports |

| 8 th | 1 st | Brief discussion about various memory used in computer |
|------------------|-----------------|---|
| | 2 nd | Primary Memory |
| | 3 rd | Secondary Memory |
| | 4 th | Memory Speed and Access time |
| 9 _{th} | 1 st | Hard Disk and its working principles |
| | 2 nd | |
| | 3 rd | - CI) IIVI, EXICINAL DITAGO |
| | 4 th | Removal storage devices – OD, DVD, Removal storage devices – Memory Stick, Flash Drive, |
| | 4 | SSD (Solid State Drive) |
| 10 th | 1 st | Koyboard - Interfacing, USB, Wileless, Types and Types |
| | 1 | Kayboard Matrix, Key Bourleing |
| | 2 nd | Interfacing IISB VVIIEIESS |
| | 3 rd | Printer – Types, operation and Trouble shooting |
| | 4 th | Scanner - Types, operation and Treasure |
| 11 th | 1 st | Displays and Graphics Cards |
| | 2 nd | TET LED DISDIAYS |
| | 3 rd | OMBS (Pasic Principles & Operations, Cr. 1997) |
| | 4 th | DIOC/ Functions setups, types of biocy |
| 12 th | 1 st | - a a T (Overetion Faults related to Flandware) |
| | 2 nd | Assembly of Components of Desktop Company |
| | 3 rd | |
| | 4 th | |
| | 7 | PAM types. CPU types, Laptop Moure |
| | | diagram, Laptop Keyboard Formatting , Partitioning and installation of OS |
| 13 th | 1 st | Trouble shooting of Common ly faced problems in |
| | 2 nd | |
| | ard | Desktops and Laptops Basic Maintenance concepts(Preventive, Corrective, |
| | 3 rd | online) |
| | 4 th | Discreption programs and tools |
| 1 4 th | 1 st | Methods of Trouble shooting(symptom observation, |
| 14 th | 199 | analysis diagnosis Correction) |
| | 2 nd | Up gradation of system and application software |
| | 3 rd | Virus concepts, Antivirus |
| | 4 th | Network Interface card |
| 15 th | 1 st | Networking interconnecting devices such as hub, switch, |
| | | Router Types of Network cable – Twisted Pair and Co-axial |
| | 2 nd | Types of Network cable — Twisted Fair and 55 axia. |
| | 3 rd | Types of Network cable – optical fiber cable |
| | 4 th | Types of Network connector |

Signature of Faculty

Signature of HOD (CSE)