

LESSON PLAN

Name of the Institute:	C. V. Raman Polytechnic	
Department:	Computer Science & Engineering	
Semester/Branch:	3 rd Sem/CSE	
Subject Name with code:	Computer System Architecture (TH-1)	
Total No. of Class (Required):	60	
Faculty Name:	Amardeep Das	
Class No.	<i>Brief description of the Topic/Chapter to be taught</i>	Remarks
1	Basic Structure of computer hardware	
2	Functional Units	
3	Computer components	
4	Computer components	
5	Performance measures	
6	Memory addressing & Operations	
7	Instructions & instruction Sequencing	
8	Fundamentals to instructions	
9	Operands	
10	Op Codes	
11	Instruction formats	
12	Addressing Modes	
13	Addressing Modes	
14	Processor System	
15	Register Files	
16	Register Files	
17	Complete instruction execution, Fetch	
18	Decode	

19	Execution	
20	Hardware control	
21	Micro program control	
22	Micro program control	
23	Hardware control VS Micro program control	
24	Memory System	
25	Memory characteristics	
26	Memory characteristics	
27	Memory hierarchy	
28	Memory hierarchy	
29	RAM and ROM organization	
30	RAM and ROM organization	
31	Interleaved Memory	
32	Cache memory	
33	Virtual memory	
34	Input Output System	
35	Input - Output Interface	
36	Modes of Data transfer	
37	Modes of Data transfer	
38	Programmed I/O Transfer	
39	Programmed I/O Transfer	
40	Interrupt driven I/O	
41	Interrupt driven I/O	
42	DMA	
43	I/O Processor	
44	I/O Interface & Bus architecture	
45	I/O Interface & Bus architecture	

46	Bus and System Bus	
47	Bus and System Bus	
48	Types of System Bus,Data	
49	Types of System Bus,Address	
50	Types of System Bus,Control	
51	Bus Structure,Basic Parameters of Bus design	
52	SCSI	
53	USB	
54	Parallel Processing	
55	Parallel Processing	
56	Linear Pipeline	
57	Linear Pipeline	
58	Multiprocessor	
59	Multiprocessor	
60	Flynns Clasification	


Signature of the Faculty


Signature of the H.O.D