

**C.V. RAMAN POLYTECHNIC, BHUBANESWAR**  
**TIME TABLE W.E.F. 15.07.2024**

**SEMESTER :- 5TH**

**BRANCH:- ETC**

Day/time	07:30-08:25	08:25-09:20	9:40-10:35	10:35-11:30	11:30-12:25	12:25-1:20	1:20-2:15	2:15-3:10
MON	VLSI (SS) RN - 06	WP (PR) RN - 06	VLSI LAB (PR, BJ)			<b>BREAK</b>	EMST (SP) RN - 06	PE & PLC (SD) RN-08
TUE	A & D LAB (SS, JD)		PE & PLC (SD) RN-08	A & D (PR) RN - 06	PROJECT PHASE - 1 (SS)			
WED	VLSI (SS) RN - 06	WP LAB (PR, BJ)			EMST (SP) RN - 06		A & D (PR) RN - 06	
THU	WP (PR) RN - 06	A & D (PR) RN - 06	PE & PLC (SD) RN-08	VLSI (SS) RN - 06	EMST (SP) RN - 06		PROJECT PHASE - 1 (PR, JD)	
FRI	A & D (PR) RN - 06	WP (PR) RN - 06	EMST (SP) RN - 06	PE & PLC (SD) RN-08	SCA (SS) ETC LAB		VLSI (SS) RN - 06	
SAT	WP (PR) RN - 06	A & D (PR) RN - 06	PE LAB (PM, DP)				SCA (SS) ETC LAB	

Copy to : Notice board/File/Principal/Concerned Department

SUBJECTS(THEORY/PRACTICAL)

- EMST
- A& D COMMUNICATION
- VLSI & EMBEDDED SYSTEM
- WEB PROPAGATION & BROADBAND COMMUNICATION ENGINEERING
- POWER ELECTRONICS & PLC

PRACTICAL

- A & D LAB  
VLSI LAB  
WP LAB  
PE LAB

PR PHASE-I

SCA

SUBJECT NAME

- ENTREPRENEURSHIP & MANAGEMENT & SMART TECHNOLOGY  
ANALOG & DIGITAL COMMUNICATION  
3.VLSI & EMBEDDED SYSTEM  
4.WEB PROPAGATION & BROADBAND COMMUNICATION ENGINEERING  
5.POWER ELECTRONICS & PLC

SUBJECT NAME

- ANALOG & DIGITAL COMMUNICATION LAB  
VLSI & EMBEDDED SYSTEM LAB  
WEB PROPAGATION & COMMUNICATION ENGINEERING LAB  
POWER ELECTRONICS LAB

PROJECT PHASE-I

STUDENT CENTERED ACTIVITES

PERIODS

- 4  
5  
4  
4  
4

PERIODS

- Name of the faculty  
SUCHISMITA SATAPATHY (SS), JULLY DAS (JD)  
PRABHAKAR RATH (PR), JULLY DAS (JD)  
PRABHAKAR RATH(PR), JULLY DAS(JD)  
3  
3  
3  
3  
4  
2



(Principal,CVRP)

Approved By