

C.V. RAMAN POLYTECHNIC, BHUBANESWAR

TIME TABLE W.E.F. 14.07.2025

SEMESTER :- 5TH

BRANCH:- ETC

SEMESTER - 1			SEMESTER - 2					
Day/time	7:40-8:40	8:40-9:40	B R E A K	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00
MON	PE & PLC (RRM) RN- 07	WP (PR) RN - 07		VLSI LAB(SS&BJ)			B R E A K	EMST (DP) RN - 07
TUE	VLSI (SS) RN - 07	PE & PLC (RRM) RN- 07		A&D LAB(SP&JD)				A & D (SP) RN - 07
WED	EMST (DP) RN - 07	VLSI (SS) RN - 07		WP LAB(PR&BJ)				A & D (PR) RN - 07
THU	VLSI (SS) RN - 07	PE & PLC (RRM) RN- 07		A & D (SP) RN - 07	WP (PR) RN - 07	ROJECT PHASE-I(SP ETC LAB		EMST (DP) RN - 07
FRI	EMST (DP) RN - 07	WP (PR) RN - 07		A & D (SP) RN - 07	PROJECT PHASE-I(SS&PR) ETC LAB			WP (PR) RN - 07
SAT	PE & PLC (RRM) RN- 07	VLSI (SS) RN - 07		PE LAB(RRM & MP)				A & D (SP) RN - 07

Copy to : Notice board/File/Principal/Concerned Department

SUBJECTS(THEORY/PRACTICAL)

1. EMST
- 2.A& D COMMUNICATION
- 3.VLSI & EMBEDDED SYSTEM
- 4.WEB PROPAGATION & BROADBAND COMMUNICATION ENGGERING
- 5.POWER ELECTRONICS & PLC

PRACTICAL

A & D LAB

VLSI LAB

WP LAB

PE LAB

PR PHASE-I

SUBJECT NAME

- 1.ENTREPRENEURSHIP & MANAGEMENT &
- 2.ANALOG & DIGITAL COMMUNICATION
- 3.VLSI & EMBEDDED SYSTEM
- 4.WEB PROPAGATION & BROADBAND COMMUNICATION ENGINEERING
- 5.POWER ELECTRONICS & PLC

SUBJECT NAME

ANALOG & DIGITAL COMMUNICATION LAB

VLSI & EMBEDDED SYSTEM LAB

WEB PROPAGATION & COMMUNICATION ENGINEERING

POWER ELECTRONICS LAB

PROJECT PHASE-I

Name of the faculty

DEBI PRASAD PANDA (DPP)

SABYASACHI PATRA(SP)

SUCHISMITA SATAPATHY (SS)

PRABHAKAR RATH (PR)

RASHMI RANJAN MARTHA(RRM)

Name of the faculty

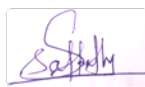
SABYASACHI PATRA(SP), JULLY DAS (JD)

SUCHISMITA SATAPATHY (SS), BHAGABATI JENA (BJ)

PRABHAKAR RATH(PR), JULLY DAS(JD)

RASHMI RANJAN MARTHA(RRM),
MAMATA PALEI(MP)

PRABHAKAR RATH(PR), SUCHISMITA SATAPATHY(SS),SABYASACHI PATRA(SP)



HOD, ETC



PRINCIPAL