

LESSON PLAN

Name of the Institute:		C. V. Raman Polytechnic
Department:		ELECTRONICS & TELECOMMUNICATION ENGINEERING
Semester/Division/Branch:		3 rd SEM/ETC & CSE
Subject Name with code:		DIGITAL ELECTRONICS(TH-3)
Total No. of Class (Required):		60
Faculty Name:		SUCHISMITA SATPATHY
Class No.	Brief description of the Topic/Chapter to be taught	Remarks
1	List different number system & their relevance : binary, octal, decimal, Hexadecimal	
2	Study the Conversion from one number system to another	
3	Perform Arithmetic operations of binary number systems.	
4	Represent the Concept of complemently numbers : 1's & 2's complement of Binary numbers.	
5	Perform Subtraction of binary numbers using complementary numbers.	
6	Perform multiplication and division of binary numbers.	
7	Define concept of Digital Code & its application.	
8	Distinguish between weighted & non-weight Code.	
9	Study Codes : definition, relevance, types (BCD, Gray, Excess-3 and ASCII code. and applications.	
10	Generation of Error Detection & Correction Code using parity bit.	
11	Learn the Basic Logic gates (NOT, OR, AND, NOR, NAND, EX-OR & EXNOR) – Symbol, function, expression, truth table & example IC nos.	
12	Define Universal Gates with examples & realization of other gates.	
13	Understand Boolean : constants, variables & functions.	
14	Comprehend the Laws of Boolean algebra, State and prove Demorgan's Theorems.	
15	Represent Logic Expression : SOP & POS forms & conversion.	
16	Simplify the Logic Expression/Functions (Maximum of 4 variables) : using Boolean algebra and Karnaugh's map methods.	
17	What is don't care conditlons ?, Realisation of simplified logic expression using gates.	
18	Illustrate with examples the above.	

19	Doubt Clearing class for Unit-I.	
20	Doubt Clearing class for Unit-I.	
21	Define a Combinational Circuit and explain with examples. Arithmetic Circuits (Binary).	
22	Truth table & applications of Half-adders, Full-adder & full-Subtractor.	
23	Multiplexure & De-Multiplexure.	
24	Discuss Decoders : definition, relevance, gate level of circuit of simple decoders, Logic circuit of high order encoders, truth table & example IC nos.	
25	Digital Comparator,Seven segment Decoder.	
26	Explain the working of Binary-Decimal Encoder & Decoder.	
27	Doubt Clearing class for Unit-II.	
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29	Define Flip-Flop.Study RS, Clocked RS, D, T, JK, MS-JK flip-flop with logic Circuit and truth tables.	
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32	Define Flip-Flop.Study RS, Clocked RS, D, T, JK, MS-JK flip-flop with logic Circuit and truth tables.	
33	Concept of Racing and how it can be avoided.	
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35	Doubt Clearing class for Unit-III.	
36	Explain the working of various types of shift registers – ISO, SIPO, PISO, PIPO.	
37	Universal shift registers-Applications.	
38	Types of Counter & applications.	
39	Binary counter, Asynchronous ripple counter (UP & DOWN), Decade counter. Synchronous counter, Ring Counter.	
40	Concept of memories-RAM, ROM, static RAM, dynamic RAM,PS RAM.	
41	Basic concept of PLD & applications.	
42	Concept of memories-RAM, ROM, static RAM, dynamic RAM,PS RAM.	
43	Doubt Clearing class for Unit-IV.	

44	Necessity of A/D and D/A converters.	
45	D/A conversion using weighted resistors methods.	
46	D/A conversion using R-2R ladder (Weighted resistors) network.	
47	A/D conversion using counter method.	
48	A/D conversion using Successive approximate method.	
49	Doubt Clearing class for Unit-V.	
50	Various logic families & categories according to the IC fabrication process.	
51	Various logic families & categories according to the IC fabrication process.	
52	Characteristics of Digital ICs- Propagation Delay.	
53	fan-out, fan-in, Power Dissipation, Noise Margin.	
54	Power Supply requirement & Speed with Reference to logic families.	
55	Features, circuit operation & various applications of TTL (NAND).	
56	Features, circuit operation & various applications of CMOS (NAND & NOR).	
57	Doubt Clearing class for Unit-VI.	
58	Previous Year Semester Question discussion.	
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Signature of the Faculty

Signature of the H.O.D