4TH SEM./ELECTRICAL./EEE/EE(INST. & CON.)/ 2024(S)

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			TH-2	Analog Electronics and OPAMP		
Full N		Marks: 80 Answer any five Questions including Q No.1& 2 Figures in the right-hand margin indicates marks			ime- 3 Hrs	
1.	a. b. c.	Mention the	r and Avala advantage	anche break down voltage. es of negative feedback. rion for sustained oscillation.	2 x 1	
	d.	Why FET is	called unip	olar device and BJT is called bipolar device?		
	e.	Define stabi	lization and	d stability factor.		
	f.	Draw the eq	uivalent ci	rcuit of OP-AMP.		
	g.	Why CE con	figuration i	is most popular in amplifier circuit?		
	h.	List the cha	racteristics	of ideal OP-AMP.		
	i.	Define and	classify tra	nsistor biasing.		
The state of the s		State the di	ifference be	etween voltage and power amplifier.		
2.	a. b.	With a nea	function o	tions plain the working of inverting and non inverting OP- of filter circuit in rectifier? Explain the working of	6 x 5	
	c. d. e. f.	(i) RMS cur Derive the transistor. Discuss the With neat	rrent and vorelationshi	of bridge rectifier and calculate oltage (ii) Ripple factor, (iii) Efficiency. In particular, possible factor, (iii) Efficiency. In particular, Γ is possible factor of Γ is a possible factor of Γ in Γ is a possible factor of Γ in Γ is a possible factor of Γ in	ng	
	g			Explain the working of Wein bridge oscillator.	ut 10	
3		ahamaetari	ctics	of transistor configuration with input and outp		
4 5		Explain the expression Define DC	ne working n for its out drain resis	g of a integrator and differentiator and derive the tput voltage. stance, AC drain resistance and trans-conductance of working of FET.	10	
6		With near	t diagram e ency respon	xplain the working of a class B push pull amplifier with	10 10	

What is clamping circuit? Explain the function of positive clamper and

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negative clamper.