



# C. V. RAMAN POLYTECHNIC

(Affiliated to SCTE & VT and Approved by Govt. Odisha)  
BIDYANAGAR, MAHURA, JANLA, BHUBANESWAR-752054, ODISHA

Ref. No.: CVRP / 564 / 2017

Date: 17/11/2017

## NOTICE

PRACTICAL PROGRAMME FOR 2<sup>ND</sup> SEMESTER (BACK)

WINTER-2017 EXAMINATION

DATE	1 <sup>ST</sup> SITTING 9.45 AM TO 12.45 PM	2 <sup>ND</sup> SITTING 01.45 PM TO 4.45 PM
22-11-2017	Engg. Chemistry Practical (ALL CIVIL, CSE, EE & ETC BACK)	Engg. Chemistry Practical (ALL MECH BACK)
	Workshop Practice (ALL Back)	Professional English & Seminar (All Back)
23-11-2017	Engg. Physics Practical (ALL Back)	Computer Application Lab (All Back)

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## NOTICE

### PRACTICAL PROGRAMME FOR 3<sup>RD</sup> SEMESTER (Regular & Back) WINTER-2017 EXAMINATION

Date	BRANCH	1 <sup>ST</sup> SITTING (9.45-12.45pm)	2 <sup>ND</sup> SITTING (1.45-4.45pm)
24-11-2017	CIVIL	Civil Engg. Lab-I (All Regular)	Civil Engg. Lab-I (All Back)
	CSE	Data Structure Lab Using C (All CSE)	xxx
	EE	Analog Electronics Lab (All EE)	Circuit Theory Lab (All EE)
	ETC	Circuit Simulation Lab (All ETC)	Analog Electronics-I Lab (All ETC)
	MECH	ME Lab (M-1 & ALL BACK)	
W/S Practice-II (M-2)		ME Lab (M-2)	
25-11-2017	CIVIL	Survey Practice-I (All Regular)	Survey Practice-I (All Back)
	CSE	Management Information System Lab (All CSE)	xxx
	EE	xxx	xxx
	ETC	Digital Electronics Lab (All ETC)	Electronics Measurement Lab (All ETC)
	MECH	ME Lab (M-3)	
W/S Practice-II (M-4)		ME Lab (M-4)	
27-11-2017	CSE	Digital Electronics Lab (All CSE)	xxx
	EE	ME Lab (All EE)	xxx
	ETC	C Programming & Web Page Design Lab (All ETC)	xxx
	MECH	xxx	xxx

Following is the distribution of Registration numbers of different groups for attending the Practical examination

Civil :- All Civil (Regular & Back) Elect.:- All Electrical (Regular & Back) ETC :- All ETC (Regular & Back) CSE:- All CSE (Regular & Back)	<b>Mechanical</b>
	ME- 1:- F16029004001 – 4030
	ME- 2:- F16029004031 – 4060
	ME- 3:- F16029004061 – 4090
	ME- 4:- F16029004091 – 4103 & L17029004001 – 4007

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Ref. No.: CVRP / 566 / 2017

Date: 17/11/2017

## NOTICE

### PRACTICAL PROGRAMME FOR 5<sup>TH</sup> SEMESTER (Regular & Back) WINTER-2017 EXAMINATION-2017

Date	BRANCH	1 <sup>ST</sup> SITTING (9.45-12.45pm)	2 <sup>ND</sup> SITTING (1.45-4.45pm)
28-11-2017	CIVIL	Civil Engg. Lab-II (All Regular)	Civil Engg. Lab-II (All Back)
	CSE	Graphics & Multimedia Lab (All CSE)	XXX
	EE	Microprocessor lab (All EE)	Electrical lab. Practice-II (All EE)
	ETC	VLSI Lab (All ETC)	Technical Seminar & Library Study (All ETC)
	MECH	Heat Power Lab (M-1 & ALL BACK)	Theory of Machines and Measurements Lab (M-1 & ALL BACK)
		Computer Programming Lab (M-2)	XXX
29-11-2017	CIVIL	CADD lab (All Regular)	CADD lab (All Back)
	CSE	Database Management System Lab (All CSE)	Programming in Java Lab (All CSE)
	EE	Power electronic lab (All EE)	XXX
	ETC	Power electronic lab (All ETC)	Information Search, Analysis & Presentation (ISAP) Lab (All ETC)
	MECH	Heat Power Lab (M-2)	Theory of Machines and Measurements Lab (M-2)
		Computer Programming Lab (M-1 & ALL BACK)	Computer Programming Lab (M-3)
30-11-2017	CSE	Programming in JAVA Lab (All CSE)	XXX
	ETC	Communication Engg.-II Lab (All ETC)	XXX
	MECH	Heat Power Lab (M-3)	Theory of Machines and Measurements Lab (M-3)

**The following is the distribution of Registration numbers of different groups for attending the practical examination**

<b>CIVIL:-</b> All Civil (Regular & Back) <b>ELECTRICAL:-</b> All Electrical ( Regular& Back) <b>ETC :-</b> All ETC (Regular & Back) <b>CSE:-</b> All CSE (Regular & Back)	<b>Mechanical</b>
	<b>M-1:-</b> F15029004001-4038
	<b>M-2:-</b> F15029004039-4076
	<b>M-3:-</b> F15029004077-4098 & L16929004001-4017

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Ref. No.: CVRP / 567 / 2017

Date: 17/11/2017

## NOTICE

**PRACTICAL PROGRAMME FOR 6<sup>TH</sup> SEMESTER (Back)**  
**Winter-2017 EXAMINATION-2017**

Date	BRANCH	1 <sup>ST</sup> SITTING (9.45-12.45pm)	2 <sup>ND</sup> SITTING (1.45-4.45pm)
20-11-2017	CIVIL	Survey Practice-II (All Back)	Project & seminar (All Back)
	CSE	Project work & Seminar (All Back)	CM & N Lab (All Back)
	EE	Electrical works Pr (All Back)	Project work & Seminar (All Back)
	MECH	AE Lab (All Back)	Project & seminar (All Back)
21-11-2017	CIVIL	XXX	XXX
	CSE	Web Dev. Lab (All Back)	XXX
	EE	Simulation Pr. on MAT Lab (All Back)	XXX
	MECH	XXX	XXX

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